

RESUME

Dr.A.SABANAYAGAM

17,VARMA CITY,
MANNARPALAYAM PRIVU ROAD,SALEM-636017
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OBJECTIVE:

To become a reputed professional who constantly updates his knowledge and applies his skills to serve the reputed institute and helps it to achieve its goals so as to have a very successful and distinguished career.

EDUCATIONAL QUALIFICATIONS:

Course	Year	Institution	class/Percentage/CGPA
Ph.D (VLSI DOMAIN)	2011	MADURAI KAMARAJ UNIVERSITY	EXCELLENT COMMENDABLE
M.E., (APPLIED ELECTRONICS)	1992	COIMBATORE INSTITUTE OF TECHNOLOGY,CBE	HIGH FIRST CLASS
B.E., (ECE)	1976	PSG COLLEGE OF TECH, CBE	HIGH SECOND CLASS

EXPERIENCE:

Teaching: 37 Years Experience

- Working as a Senior Professor in ECE Department, Vivekanada Institute of Engg &Tech for Women (VIETW), Thiruchengode,. Since June 2016
- Worked as a Sr.Professor in ECE Department,VELTECH MULTITECH COLLEGE OF ENGG,AVADI, Dec 2015-May2016
- Worked as Principal, Gnanamani college of engg,Namakkal,Nov2014 –Nov 2015
- Worked as principal, Sri vidya college of engg,virudhunagar,April2012-Sep 2014
- Worked as Lecturer, Sr Lecturer, HOD &Principal in Govt Polytechnic colleges 1980-2012.

Industrial: Four Years of Experience

- Worked as an Apprentice Trainee in TNEB, As Maintenance Engineer in Various Industries including MKU June1976 – Feb1980.

SUBJECTS TAUGHT:

- UG:** ECE Students
Principles of Management, Control Systems, Electro Magnetic Theory, Digital Electronics, Analysis and Design of Analog ICS, Digital logic Circuits,
- PG:** **Applications of MEMS technology**, Solid state Device Modeling and Simulation, Analysis and Design of Analog ICS

WORKSHOP/SEMINAR ATTENDED:

- ✚ National Workshop –15 nos,
- ✚ National Seminar on Research -15 nos
- ✚ International Symposium on “ICT in Education changes and Innovations” 12th March 2009 at NITTTR,Chennai

WORKSHOP ORGANIZED:-10 nos

- ✚ One day Workshop on “Digital Signal Processing based Control Applications”,
- ✚ Two day Workshop on “LabVIEW based System Design”,
- ✚ Contributed as a Resource Speaker in Entrepreneurship Development Programme on “Computer hardware Maintenance” ,
- ✚ Two day Workshop on “VLSI Design”,
- ✚ One day Workshop on “Fuzzy logic and Genetic algorithm using MatLab”,

ACHIEVEMENTS:

- ✚
- ✚
- ✚

KEY STRENGTHS:

- ✚ Adaptive to any situation, Self motivation, Outspoken and Ability to work efficiently as an individual and also in teams

PROJECT GUIDED:

- ✚ No. of UG & PG Project Guided: 05 Projects per Year
- ✚ No of Ph.D's Guiding-2 nos

Field of Interest: Embedded systems, VLSI Design, MEMS, DIP

- ✚ No. of Paper published in National Conference : 10
- ✚ No of papers Published in International conference:05

RESPONSIBILITIES IN COLLEGE:

- ✚ Experience as Time table co-ordinator, Faculty Advisor, Project co-ordinator, AU Practical Examination Dept Co-ordinator, Placement Officer, R&D Co-ordinator, NAAC, NBA Trainer.

MEMBERSHIP IN PROFESSIONAL ORGANISATION:**LIFE MEMBER IN**

- (1) Institute Of Electronics telecommunications Engineers (Fellow Member), New Delhi (Fiete)
- (2) Institute Of Engineers (India), Calcutta. (Fie)
- (3) Indian Society for Technical Education, New Delhi.
- (4) Society of Emc Engineers (India), Bangalore. (Semce (I))
- (5) Was Student Member in IEEE, USA
- (6) Member in Productivity Council, Management Association.

EXPERIENCE DETAILS

INDUSTRIAL EXPERIENCE	:	4 years
TEACHING EXPERIENCE	:	36years
EXPERIENCE AS PRINCIPAL	:	7 years
RESEARCH EXPERIENCE	:	15Years

AREAS OF INTEREST:

COMPUTATIONAL ELECTROMAGNETICS, VLSI DESIGN.

BOOKS PUBLISHED:

AUTHORED MANY BOOKS ON

- ✚ DIGITAL ELECTRONICS
- ✚ ELECTRONIC CIRCUITS
- ✚ COMMUNICATION ENGINEERING
- ✚ COMMUNICATION SYTEMS
- ✚ COMPUTER APPLICATIONS
- ✚ C PROGRAMMING AND OOPS. ETC.,

OTHER EXPERIENCES:

- ✚ ATTENDED MANYWORKSHOPS, PARTICIPATED IN MANY SEMINARS AT NATIONAL, INTERNATIONAL LEVEL AND PUBLISHED PAPERS. ACTED AS BOARD OF STUDIES MEMBERS FOR VARIOUS POLYTECHNIC COLLEGES.
- ✚ STATE BOARD OFTECHNICAL EDUCATION, CHENNAI, SYLLABUS COMMITTEE MEMBER.
- ✚ WINNER OF BEST PROJECT GUIDANCE AND HAVE GUIDED MANY POLYTECHNIC PROJECTS AND AMIE PROJECTS.
- ✚ VISITING PROFESSOR FOR MANY ENGINNERING COLLEGES. GUIDANCE AND COUNCELLING MEMBER.
- ✚ VISITING PROFESSOR IN ANNA UNIVERSITY, MADURAI
- ✚ RECEIPTANT OF EMINENT ENGINEER AWARD FROM IE(I)
- ✚ EC MEMBER IN ISTE,IE(I).
- ✚ Ph.D GUIDESHIP: (1) St...PETERS UNIVERSITY, CHENNAI
(2) AMET UNIVERSITY, CHENNAI.
(3) Dr.K.N.MODI UNIVERSITY, RAJASTHAN.
- ✚ PROJECTS GUIDED : (1) M.E:5
(2)M.B.A:3
- ✚ PROJECTS GUIDING: Ph.D:03
- ✚ R&D FUNDED PRO FROM IE(I),KOLKATTA-Rs35000/-
- ✚ ACTED AS AICTE INSPECTION COMMITTEE MEMBER TWICE.
- ✚ ACTED AS CURRICULAM EVALUATION COMMITTEE MEMBER.
- ✚ VISITING FACULTY FOR TN VETERINARY &ANIMAL UNIVERSITY.
- ✚ ACTED AS CHAIR PERSION FOR MANY INTERNATIONAL CINFERENCES (IEEE).
- ✚ ACTED AS CHIEF GUEST &JURY FOR IE(I) PROJECTS EVALUATION & SCIENCE EXIBITION
- ✚ ACTED AS INSPECTION COMMITTEE MEMBER FOR AICTE MODROB SCHEME.

- ✚ WORKED AS DTE REPRESENTATIVE FOR VARIOUS POLYTECHNIC COLLEGES ADMISSIONS.
- ✚ FDP CONDUCTED: AS CONVENER : 13 NOS
AS CO-ORDINATOR : 03 NOS
FDP ATTENDED : 20 NOS
- ✚ ACTED AS DOCTORAL COMMITTEE MEMBER-FOR ANNA UNIVERSITY
- ✚ RECEIPTANT OF GOOD APPRECIATION FROM
(1)MANY NGO'S
(2)CO-OPERATIVE SOCIETIES,
(3)IE (I)
- ✚ CONFERENCE/SEMINAR/WORKSHOP CONDUCTED DETAILS:
(1)NATIONAL CONFERENCES-05 NOS
(2)INTERNATIONAL CONFERENCES-02 NOS

JOURNAL PUBLICATIONS

(1)INTERNATIONAL:

- ✚ OPTIMISATION OF INTERCONNECTS FOR SIGNAL INTEGRITY USING SELF-ADAPTION IN REAL PARAMETER GENETIC ALGORITHM WITH SIMULATED BINARY CROSS OVER(SBX)-2008-PROC OF 10TH INTERNATIONAL CONFERENCE ON EMI(C).
- ✚ SIGNAL INTEGRITY ANALYSIS FOR HIGH SPEED ON-CHIP INTERCONNECTS USIHNG DIFFERENTIAL ALGORITHM-2008-PROC OF INT.CONFERENCE ON EMI&C
- ✚ "DIGITAL ELECTRONICS-PP147-166.
- ✚ DESIGN &REALIZATION OF NON-VOLATILE SRAM BASED ON MAGNETIC TUNNEL JUNCTION-AMERICAN JOURNAL OF ENGG RESEARCH (AJER)-2013-ISSN: 2320-0847, 2320-0936.
- ✚ "A NEW HYBRID TOPOLOGY FOR MULTILEVEL CASCAD INVERTER DESIGNWITH POD MODULATION TECHNIQUE-INTELLECTUAL ADVANCEMENTS &RESEARCH IN ENGG COMPUTATIONS (IJIAREC-15-116)-ISSN: 2348-2079.
- ✚ HIGH POWER FACTOR SINGLE SWITCH AC TO DC CONVERTER FOR LED LIGHTING'-IEEJ TRANSANCTIONS ON ELECTRICAL&ELECTRONICS ENGINEERING,JAPAN.
- ✚ "A NEW HYBRID TOPOLOGY INVERTER DESIGN &OPTIMISATION-ISSN 2348-2079.-IEEE JOURNAL 2015
- ✚ "LOW POWER DESIGN TECHNIQUES IN VLSI-INDIAN ENGG CONGRESS-2013, 20-22 BY IE(I),KOLKATTA.

(2)NATIONAL:

- ✚ PERFORMANCE ANALYSIS,MODELLING AND POWER OPTIMISATION IN SOLAR POWERED AUTOMATED DRIER-St.PETR'S UNIVERSITY RESEARCH JOURNAL-ISSN:978-81-927500-5-7.
- ✚ ENERGY MANAGEMENT-WATER &ENERGY-JOURNAL OF RESEARCH BY IE (I)-MARCH2014.
- ✚ LOCUS OF CONTROL &ACADEMIC ACHIEVEMENTS OF STUDENTS-MAY 2013 -JOURNAL OF SCHOOL SOCIAL WORK,CHENNAI-ISSN 0976-3759;

CONFERENCE PUBLICATIONS

(1)INTERNATIONAL:

- ✚ SIGNAL INTEGRITY ANALYSIS FOR HIGH SPEED ON-CHIP INTERCONNECTS USING DEA-ICSIOT-2013
- ✚ "HYBRID METHOD FOR DENOISING&IMAGE RESTORATION-NCATEE, 2014.

(2) NATIONAL:

- ✚ CNC BASED LINEAR WELDING MACHINE-ICSEP, 2013.
- ✚ "FIRE CHEMICAL WORKS &SAFETY PRECAUTIONS-PYROTECH- 2013

BOOKS PUBLISHED:

- ✚ COMPUTER HARDWARE&N/W-M/S ROYAL PUBLICATIONS.
- ✚ BIO-MEDICAL &TELEMEDICINE-M/S ROYAL PUBLICATIONS.
- ✚ DIGITAL ELECTRONICS-M/S JJ PUBLICATIOIS.
- ✚ COMPUTER APPLICATIONS LAB MANUAL-M/S JD PUBLICATIONS
- ✚ ANALOG &DIGITAL ELECTRONICS-M/S JJ PUBLICATIONS
- ✚ ELECTRONICS CIRCUITS-M/S JJ PUBLICATIONS
- ✚ MICRO ELECTRONICS-M/S JJ PUBLICATIONS
- ✚ COMMUNICATION ENGG-M/S JD PUBLICATIONS
- ✚ COMMUNICATION SYSTEMS-M/S JD PUBLICATIONS
- ✚ 'C PROG &OOPS'-M/S JS PUBLICATIONS
- ✚ ELECTRICAL &ELECTRONICS ENGG-M/S JJ PUBLICATIONS
- ✚ ELECTRICAL CIRCUITS AND MEASUREMENTS-M/S JS PUBLICATIONS.

PERSONAL DETAILS:

Father's Name : Dr.A.SABANAYAGAM

Date of Birth : 07.03.1954

Languages Known : Tamil, English and HINDI,

Hobbies : Listening music

Present Address : 17,VARMA CITY,
MANNARPALAYAM PRIVU ROAD
SALEM-636017.

Contact Number : 9344132508(Mobile)

DECLARATION:

I hereby declare that all the details furnished above are true to the best of my knowledge and belief.

Place : SALEM

Date :

(Dr.A.SABANAYAGAM)